Docket No.	
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## COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that: My residence, post office address and citizenship are as stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled BIPOLAR TRANSISTOR AND METHOD FOR FABRICATING THE SAME, the specification of which (check one) X is attached hereto. was filed on Application Serial No. I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a). I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed: Prior Foreign Application(s) **Priority Claimed** 2003-082421 JAPAN 25/03/2003 X Yes No (Number) (Day/Month/Year Filed) (Country) \_\_\_ Yes \_\_ No (Day/Month/Year Filed) (Number) (Country) \_\_\_ Yes \_\_ No (Number) (Country) (Day/Month/Year Filed)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

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(Appln. Serial No.)	(Filing I	Date)	(Status-patented, pending, abandoned)
(Appln. Serial No.)	(Filing I	Date)	(Status-patented, pending, abandoned)
the patent application Trademark Office con (Reg. No. 28,149); K Paul Devinsky (Reg.	on identified about the innected therewith L. Cage No. 28,553); I 5,139); Brian E. I	ove and to trach: h: Raphael V. (Reg. No. 26, 1 Edward E. Ki Ferguson (Reg	r of substitution and revocation, to prosecute ansact all business in the U.S. Patent and Lupo (Reg. No. 28,363); Jack Q. Lever, Jr 151); Stanislaus Aksman (Reg. No. 28,562) ubasiewicz (Reg. No. 30,020), Michael Eg. No. 36,801); Robert W. Zelnick (Reg. No. 5).
Please address all con	respondence an	d telephone ca	alls to:
	Jack Q. Lever, McDERMOT' 600 Thirteenth Washington, I (202) 756-800	Τ, WILL & EN 1 Street, N.W. 2.C. 20005-30	
instructions from Trademark Office re attorney and the und	Maeda Patent Ogarding this appersigned. In the	ffice a dication witho event of a cha	ttorneys named herein to accept and follow as to any action to be taken in the Patent and out direct communication between the U.S ange in the persons from whom instruction be so notified by the undersigned.
all statements made statements were mad punishable by fine or	on information e with the know imprisonment, o villful false stater	and belief are ledge that will r both, under S	rein of my own knowledge are true and that believed to be true; and further that these ful false statements and the like so made are Section 1001 of Title 18 of the United State pardize the validity of the application or any
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<sup>\*</sup> City and State, or City and Country for foreign inventors

Docket No.: 60188-817

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Teruhito OHNISHI, et al.

Confirmation Number:

Serial No.:

Group Art Unit:

Filed: March 24, 2004

Examiner:

For:

BIPOLAR TRANSISTOR AND METHOD FOR FABRICATING THE SAME

## ASSOCIATE POWER OF ATTORNEY

Mail Stop Associate Power of Attorney Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The undersigned Principal Attorney of record hereby appoints the following Attorneys as his Associates with regard to the above-identified application: Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Richard E. Brown, Reg. No. 47,453; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Ramyar M. Farid, Reg. No. 46,692; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Keith E. George, Reg. No. 34,111; Thomas A. Haag, Reg. No. 47,621; John A. Hankins, Reg. No. 32,029; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Gene Z. Rubinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti, Reg. No. 39,048; Kelli N. Watson, Reg. No. 47,170; Cameron K. Weiffenbach, Reg. No. 48,488; Aaron Weisstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; William Young, Reg. No. 54,718; and Wei-Chen Chen and Tomoki Tanida, admitted under 37 CFR 10.9(b) all of

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March 24, 2004

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